Notice of Allowability	Application No.	Applicant(s)
	10/609,063	GOLDSTONE, MARC B.
	Examiner	Art Unit
	Edwin A. León	2833
The MAILING DATE of this communication app. All claims being allowable, PROSECUTION ON THE MERITS I herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	S (OR REMAINS) CLOSED i 5) or other appropriate comm RIGHTS. This application is:	n this application. If not included unication will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>Election of 10/01/0-</u>	4 and Interview Summary of 1	<u>1/10/04</u> .
2. The allowed claim(s) is/are 1-3 and 16-18.		
3. \boxtimes The drawings filed on <u>6/27/03</u> are accepted by the Exam	iner.	
 4. Acknowledgment is made of a claim for foreign priority a) All b) Some* c) None of the: 1. Certified copies of the priority documents hat 2. Certified copies of the priority documents hat 3. Copies of the certified copies of the priority of International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	ve been received. ve been received in Application	on No
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	E" of this communication to file NMENT of this application.	e a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which g	omitted. Note the attached EX ives reason(s) why the oath o	AMINER'S AMENDMENT or NOTICE OF or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") m (a) ☐ including changes required by the Notice of Draftspot 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examine Paper No./Mail Date 	erson's Patent Drawing Revie 	r in the Office action of
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
7. DEPOSIT OF and/or INFORMATION about the delattached Examiner's comment regarding REQUIREMEN	posit of BIOLOGICAL MAT IT FOR THE DEPOSIT OF BI	ERIAL must be submitted. Note the OLOGICAL MATERIAL.
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of I	nformal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413), /Mail Date <u>11/10/04</u> .
3. Information Disclosure Statements (PTO-1449 or PTO/SI Paper No./Mail Date		Amendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit		Statement of Reasons for Allowance
of Biological Material	9. 🔲 Other	- Jankilan
		Gary Paumen Primary Examiner

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DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Eric King on November 10, 2004.

The application has been amended as follows:

On Claim 1, Line 2, change "for mounting" to –mounted--; Line 3, after "cable connector" add –in a direction parallel to the PCB-- and change "configured for mounting" to –mounted--; Line 9, change "for mounting" to –mounted--; and Line 10, after "extending" add –directly--.

On Claim 16, Line 5, after "cable connector" add –in a direction parallel to the PCB--; Line 8, after "cable connector" add –in a direction parallel to the PCB--; Line 9, delete "for"; Line 11, change "for mounting" to –mounted--; and Line 12, after "extending" add –directly--.

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Allowable Subject Matter

2. Claims 1-3 and 16-18 are allowed.

The following is an examiner's statement of reasons for allowance:

Applicant's admitted prior art discloses a raised Serial Advanced Technology

Attachment (SATA) Printed Circuit Board (PCB) connector (1) for mounting to a printed circuit board (2), the raised SATA PCB connector (1) configured for connection with a SATA cable connector and configured for mounting onto a first side of a PCB (2) in opposing relation with another raised SATA PCB connector (1) similarly mounted onto a second side of the PCB (2) and also configured for connection with a SATA cable connector, the raised SATA PCB connector (1) comprising: a blade connector (3) for supporting an electrical contact arrangement compatible with a SATA standard; and a raised mounting portion (5) for mounting to a first side of a PCB (2), the raised mounting portion extending from the PCB (2). See Fig. 1.

Wu (U.S. Patent No. 6,589,074) discloses a raised Serial Advanced Technology Attachment (SATA) Printed Circuit Board (PCB) connector (1) for mounting to a printed circuit board (5), the raised SATA PCB connector (1), the raised SATA PCB connector (1) comprising: a blade connector (23) for supporting an electrical contact arrangement (3) compatible with a SATA standard; and a raised mounting portion (20, 21), the raised mounting portion (20, 21) extending from the PCB (1); wherein the blade connector (23)

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is integrally formed with the raised mounting portion (20, 21) and projects from the raised mounting portion (20, 21). See Figs. 1-7.

However, the references fail to teach, disclose, or suggest, either alone or in combination, the raised SATA PCB connector configured for connection with a SATA cable connector in a direction parallel to the PCB and the raised mounting portion extending directly from the PCB and the blade connector being integrally formed with the raised mounting portion and projecting from the raised mounting portion and in combination with the rest of the limitations of the base claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Lynch et al. (U.S. Patent No. 6,702,620), Garret et al. (U.S. Patent No. 6,811,427), Maiers (U.S. Patent No. 6,764,344) and Cheng et al. (U.S. Patent No. 6,739,883) disclose SATA printed circuit board connector having raised portions, blade connectors and printed circuit boards.

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703-872-9306.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (571) 272-

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2008. The examiner can normally be reached on Monday - Friday 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on 571-272-2800, extension 33. The fax phone number for the organization where this application or proceeding is assigned is

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Examiner

Edwin A. Leon

AU 2833

EAL November 11, 2004